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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/697,278	10/31/2003	Shigeki Yabu	02910.000098	8435
5514 7	590 09/06/2006		EXAM	INER
FITZPATRICK CELLA HARPER & SCINTO			NGUYEN, J	ENNIFER T
30 ROCKEFELLER PLAZA			ARTIBUT	DADED MIMDED
NEW YORK. NY 10112			ART UNIT	PAPER N

DATE MAILED: 09/06/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
•	10/697,278	YABU ET AL.				
Office Action Summary	Examiner	Art Unit				
	Jennifer T. Nguyen	2629				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address						
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUN 136(a). In no event, however, may a will apply and will expire SIX (6) MO e, cause the application to become a	ICATION. The reply be timely filed ENTHS from the mailing date of this communication. ABANDONED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 10/3	Responsive to communication(s) filed on <u>10/31/03</u> .					
·—	,—					
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under	Ex parte Quayle, 1935 C.	D. 11, 453 O.G. 213.				
Disposition of Claims						
4) ⊠ Claim(s) 1-14 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 1-14 is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and/or	wn from consideration.					
Application Papers						
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) acc Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Examine 11.	cepted or b) objected to drawing(s) be held in abeya ction is required if the drawin	ance. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Burea * See the attached detailed Office action for a list	ts have been received. ts have been received in a prity documents have bee ou (PCT Rule 17.2(a)).	Application No n received in this National Stage				
Attachment(s)	_					
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 2/12/04;4/4/05;	Paper No	Summary (PTO-413) (s)/Mail Date Informal Patent Application (PTO-152)				

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DETAILED ACTION

Claim Rejections - 35 USC § 112

- 1. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 2. Regarding claims 1 and 13, the phrase "a drive voltage which **may** provide a specified display state" in claims 1 and 13 and the phrase "a threshold value potential which **may** emit electrons" in claim 13 renders the claim(s) indefinite because the claim(s) include(s) elements optionally disclosed, thereby rendering the scope of the claim(s) unascertainable.

Double Patenting

3. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. A nonstatutory obviousness-type double patenting rejection is appropriate where the conflicting claims are not identical, but at least one examined application claim is not patentably distinct from the reference claim(s) because the examined application claim is either anticipated by, or would have been obvious over, the reference claim(s). See, e.g., *In re Berg*, 140 F.3d 1428, 46 USPQ2d 1226 (Fed. Cir. 1998); *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and *In re Thorington*, 418 F.2d 528, 163 USPO 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) or 1.321(d) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent either is shown to be commonly owned with this application, or claims an invention made as a result of activities undertaken within the scope of a joint research agreement.

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

4. Claims 1-14 are provisionally rejected on the ground of nonstatutory obviousness-type double patenting as being unpatentable over claims 1-14 of copending Application No. 10/716,664. Although claimed limitations of instant application are not identical with claimed

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limitations of copending application. However, the claimed limitations of instant application encompass of the claimed limitations of copending application:

10/697,278 (claim 1)	10/716,664 (claim 1)	
A display device	A display device	
a display panel having cathodes, gates, and	a display panel having cathodes, gates and an	
anodes in which said cathodes and said gates	anode, the cathodes and the gates being	
are connected in a matrix manner	connected in matrix form	
an electron emitter which may emit electrons	electron emitters provided on each of the	
in a state where a voltage is applied only	cathodes and capable of performing electron	
between said cathodes and said anodes and	emission with a voltage applied only between	
which is provided with said cathode	the cathodes and the anode	
display of each of pixels is performed under a	the display device being constructed to perform	
dark state by stopping electron emission from	display by bringing pixels to dark states by	
said electron emitter for said anodes by	applying a cut-off voltage between the	
applying a stop voltage between said cathodes	cathodes and the gates to cut off electron	
and said gates	emission from the electron emitters toward the	
	anode	
a control means which controls operation of a	a control unit for controlling the operation of a	
circuit for driving a display panel in such a	display panel driving circuit in order to	
way that a potential of each of said anodes	complete, when a display completing signal is	
becomes not less than a threshold value	generated, application of the cut-off voltage or	
potential by which said potential of each of	a driving voltage capable of providing a	

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anodes can perform electron emission from said electron emitter after a predetermined time elapse from starting of application of a drive voltage which may provide a specified display state or said stop voltage between said cathodes and said gates when a display starting signal is generated.

particular display state, after a predetermined time passes from the moment when a potential of the anode decreases below a threshold potential capable of causing electron emission from the electron emitters with the cut-off voltage or the driving voltage capable of providing the particular display state being applied between the cathodes and the gates.

This is a <u>provisional</u> obviousness-type double patenting rejection.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer T. Nguyen whose telephone number is 571-272-7696. The examiner can normally be reached on Mon-Fri: 9:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe can be reached on 571-272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications
may be obtained from either Private PAIR or Public PAIR. Status information for unpublished
applications is available through Private PAIR only. For more information about the PAIR

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system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jennifer Nguyen 8/30/06

RICHARD HJERPE

SUPERVISORY PATENT EXAMINER